IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Miwa KOZAWA et al.

Serial Number: Not Yet Assigned

Filed: November 25, 2003

Customer No.: 38834

For: PROCESS FOR FORMING RESIST PATTERN, SEMICONDUCTOR DEVICE AND FABRICATION THEREOF

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

November 25, 2003

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. <u>50-2866</u>.

Respectfully submitted,

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Enclosures: PTO-1449; References (6)

INFORMATION DISCLOSURE STATEMENT PTO-1449			Atty. De	ocket No. 03	32132	Serial No. New Appln.		
			Applicant(s): Miwa KOZAWA et al.					
			Filing D	ate: Noven	nber 25, 2003	Group Art Unit:		
U.S. PATENT DOCUMENTS								
Examiner Docum Initial			ent No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA							
	AB							
	AC							
	AD							
FOREIGN PATENT DOCUMENTS								
		Docum	nent No.	Date	Country	Translation (Yes or No		
	AE 10-739		27	03/17/98	Japan	Abstract.	et. Cited in the specification	
	AF	5-1971	51	08/06/93	Japan	Yes		
	AG	2000-3	47414	12/15/00	Japan	Yes		
	AH	2001-2	28616	08/24/01	Japan	Yes		
	ΑI	2002-6	491	01/09/02	Japan	Yes		
	AJ	2002-4	9161	02/15/02	Japan	Yes		
AK								
OTHER DOCUMENTS								
	AL							
	AM							
	AN							
Examiner Date Considered								

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